

Digital Lock-In Amplifiers

SE2041-DSP Lock-In Amplifier

DC to 80 MHz



Features

- DC to 80 MHz frequency range
- 1 nV to 1 V full-scale sensitivity
- Time constants from 100 ns to 3 ks
- >120 dB dynamic reserve
- Input noise as low as 6 nV/√Hz @100 kHz
- A high-performance signal generator
- Automatic adjustment
- Up to 6 demodulators
- 5.6 inch color TFT-LCD screen

Overview

SE2041 Digital Lock-in Amplifier provides an excellent performance within its bandwidth from DC to 80 MHz. With the advantage of the latest digital signal processing technology and high-speed 250MSPS 14-bit ADC, SE2041 can easily detect the phase and the magnitude of weak signals overwhelmed by various large noise. The performance of SE2041 is as good as other lock-in amplifiers all over the world, even better than them in some certain parameters, such as measurement accuracy, SNR, dynamic reserve, which meets the needs of scientific research and industrial application well.

Input Channel

SE2041 detects an input signal in a single-ended mode or a differential voltage mode. With an ultra low-noise preamplifier, the input noise is as low as 6 nV/√Hz@100 kHz. The input impedance is 50 Ω or 10 MΩ and the full-scale input voltage sensitivity ranges from 1 nV to 1 V. Besides, designed to eliminate power frequency interference. A programmable gain amplifier is used to adjust the dynamic reserve of the system, so that SE2041 can keep a high dynamic reserve of 120 dB. The high-precision 14-bit ADC has a sampling rate of 250 MSPS, and the excellent anti-aliasing filter in front of the ADC can effectively prevent signal aliasing.

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Reference Channel

The reference signal can work in external mode or internal mode. In internal mode, a precise and stable internal oscillator generates sine wave as an internal reference that is multiplied by the input signal. This internal signal is without any phase noise. With the digital phase-shifting technique, the phase resolution of the reference signal is 0.001 deg. SE2041 can work at any fixed frequency from DC to 80 MHz in this mode. In external mode, the reference signal can be a sine wave or a TTL pulse or square wave. The rising or falling edge of the external reference signal triggers the Phase Lock Loop (PLL) to lock the external signal. Based on the frequency of the reference signal, can demodulate multiple harmonics and arbitrarily frequency input signal. The maximum harmonic signal frequency can reach 32,767 times the fundamental frequency, and the maximum harmonic frequency cannot exceed the maximum operating frequency of the instrument by 60 MHz.

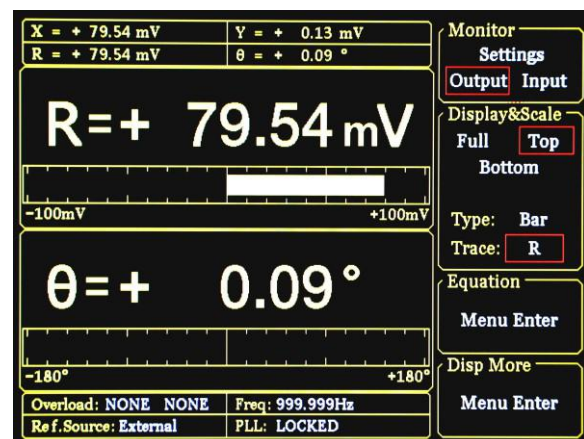
Digital Demodulator and Output Filter

The key component of the SE2041 is the digital demodulator. Compared to traditional analog lock-in amplifiers, the SE2041's internal digital demodulator effectively rejects the measurement errors caused by DC drift and offset. In addition, by optimizing the multiplication of the internal coherent signal of the digital demodulator, the calculation error is minimized so that the instrument can accurately detect the input weak

signal. Time constants of the output low-pass filter from 30 ns to 4.4 ks can be selected with a choice of 6, 12, 18, 24, 30, 36, 42 and 48 dB/oct rolloff. This low-pass digital filter is implemented using a high performance digital filter with a sample rate of 250 MHz. The digital demodulation and the low-pass filter used in SE2041 guarantees a high dynamic reserve (>120dB), accurate phase (absolute phase error <1 deg). Moreover, when the frequency of the input signal is lower than 200 Hz, A synchronous filter can be used to eliminate the harmonic influence of the reference signal, ensuring that SE2041 can detect a low-frequency signal quickly and effectively.

Display

SE2041 has a 5.6-inch 640 × 480 color TFT-LCD. The measurement results of SE2041, such as X, Y, R, and θ , are shown in numerical form and bar graph on the display.



In X-Y chart, SE2041 shows the trend of measurement results over time, and check the value by using knob control cursor.

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Internal Oscillator

The internal oscillator of SE2041 generates a low distortion (−80 dBc) sine reference signal varying from DC to 80 MHz, which has a high frequency resolution of 1 MHz. The frequency and amplitude of the reference signal can be set by using the front panel of SE2041 or communication interface. When SE2041 is set in the external reference mode, the internal reference signal is phase-locked with the external reference signal.

Signal Generator

SE2041 uses a high precision digital-to-analog converter (DAC) to output a sine wave signal at the same frequency as the internal reference signal from DC to 80 MHz. The amplitude and phase of the output sine wave can be set through the SE2041's display, where the maximum amplitude of the sine wave is 1 Vrms with 1 uVrms accuracy.

Auxiliary IO

SE2041 has many auxiliary input and output interface. AUX-IN ports can measure voltage below 10V, and their sample rate is 312.5 kSPS. AUX-OUT/CH-OUT can output X, Y, R, Xita value and arbitrary DC Volts. Otherwise, SE2041 has CLK-IN, CLK-OUT, SYNC IN, SYNC OUT and Monitor out ports.

Manual Operation

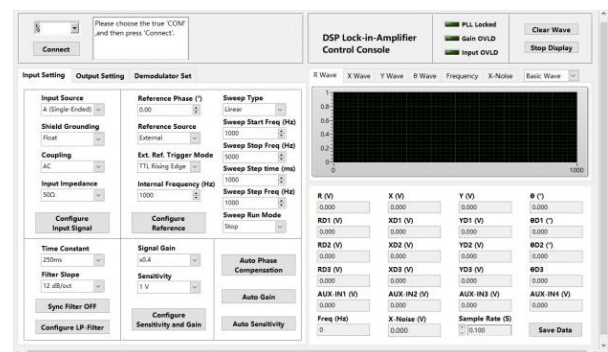
The parameters are convenient to be adjusted by the soft keys besides the display and the numeric keypad on the front panel, such as the internal oscillator frequency and the SINE OUT amplitude.

Auto Function

SE2041 can automatically adjust itself into different optimal operating modes for different input signals, such as Auto Gain mode, Auto Reserve mode and Auto Phase mode. This function makes it easier for users to measure signals more efficiently.

Remote Operation

Users can use PC to control SE2041 through communication interfaces, including setting the parameters and reading the measurement data. SE2041 is equipped with a free LabVIEW program, which makes it easy to use in complex scientific experiments.



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Technical Specifications

➤ Signal Channel

| | |
|------------------------|---|
| Voltage Input Mode | Single-ended or Differential |
| Full-scale Sensitivity | 1 nV to 2 V |
| Voltage | 50 Ω // 10 pF or 10 M Ω // 10pF,AC or DC coupled |
| C.M.R.R | >80 dB to 100 kHz, >70 dB to 1 MHz |
| Dynamic Reserve | >120 dB |
| Gain Accuracy | 0.5% typ (<1 MHz), 3% max |
| Noise | 3.5 nV/ $\sqrt{\text{Hz}}$ @ 100kHz 3 nV/ $\sqrt{\text{Hz}}$ @ 1 MHz |
| Grounding | BNC shield can be grounded or floated via 1 k Ω to ground |

➤ Reference Channel

| | |
|----------------------|---|
| Input | |
| Frequency range | DC to 80 MHz |
| Reference input | Square or Sine |
| Input impedance | 1 M Ω //10 pF |
| TTL level | $V_{\text{INH}} > 3 \text{ V}$, $V_{\text{INL}} < 0.5 \text{ V}$ |
| Sine reference level | $0.2 \text{ V} < V_{\text{PP}} < 10 \text{ V}$, Freq > 1 Hz |
| Phase | |
| Resolution | 0.001 deg |
| Absolute phase error | <1 deg typ. (<1 MHz), 5 deg max. |

| | |
|----------------------|---|
| Relative phase error | < 1 mdeg |
| Orthogonality | 90 \pm 0.001 deg |
| Phase noise | |
| Internal ref. | Synthesized, <0.0001 deg at 1 kHz |
| External ref. | 0.005 deg at 1 kHz (100 ms time constant, 12 dB/oct) |
| Drift | <0.01 deg/ $^{\circ}\text{C}$ below 100 kHz <0.1 deg/ $^{\circ}\text{C}$ above 100 kHz |
| Harmonic Detection | 2F, 3F, ...nF to 60 MHz (n<32767) |
| Acquisition Time | |
| Internal ref. | Instantaneous acquisition |
| External ref. | (100 cycles + 5 ms) |

➤ Demodulator

| | |
|---------------------|--|
| Stability | |
| Digital output | no zero drift on all setting |
| Display | no zero drift on all setting |
| Analog output | < 200 ppm/ $^{\circ}\text{C}$ |
| Harmonic Rejection | -90 dB |
| Time Constant | 100 ns to 3 ks (6,12,18,24, 30,36,42,48 dB/oct rolloff) |
| Synchronous Filters | Effective below 1 MHz with a rolloff greater than 18 dB/octave. |

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➤ Internal Oscillator

| | |
|------------|---|
| Frequency | |
| Range | DC to 80 MHz |
| Accuracy | 2 ppm + 1 μ Hz |
| Resolution | 1 mHz |
| Distortion | -75 dBFS (f=100 kHz), -60 dBFS (f=1 MHz) |
| Amplitude | 1 μ V to 2 Vrms |
| Accuracy | 0.5% typ. (<1 MHz), 3% max. |
| Stability | < 200 ppm/°C |
| Impedance | 50 Ω |
| TTL Output | 3.3V TTL/CMOS level |

➤ Display

| | |
|--------------------|---|
| Screen | 5.6 inch, 640×480 TFT |
| Screen Format | Single or dual display |
| Display Quantities | Each display shows one trace, traces can be defined as X,Y,R, θ |
| Display Types | Numerical form, bar graph |

➤ Auxiliary Inputs and Outputs

| | |
|----------------|---|
| AUX Inputs | |
| Function | 4 channel inputs |
| Voltage | \pm 10 V full scale 0.3mV resolution |
| Impedance | 1 M Ω |
| AUX/CH Outputs | |
| Function | 4 channel outputs |

| | |
|---------------|--|
| Voltage | \pm 10 V full scale 0.3 mV resolution |
| Drive Current | 30 mA max output current |

➤ Remote Interfaces

USB2.0, RS-232(DB-9) and 1000Mbps Ethernet

➤ General

| | |
|-------------------|---|
| Power Requirement | |
| Voltage | 100-240 V AC |
| Frequency | 50 (60 Hz optional) |
| Power | 50 W typ., 70W max. |
| Dimension | 448 (W)×148 (H)×513 (D) mm (with feet) 448 (W)×133 (H)×470 (D) mm (without feet) |
| Weight | 12kg |